

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Application No.: New U.S. National Stage of PCT/JP00/03051

Filed: January 2, 2001

Docket No.: 108102

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE THEREOF,  
CIRCUIT BOARD AND ELECTRONIC INSTRUMENT

*J. Stevenson*  
*H 4/a*  
*3-3501*

PRELIMINARY AMENDMENT

Director of the U.S. Patent and Trademark Office  
Washington, D. C. 20231

Sir:

Prior to initial examination, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please amend the specification as follows:

Page 15, line 6, change "a first region 12" to --a region of mounting the  
semiconductor chip 10--.

NE Page 16, line 17, change "projecting portions" to --concave portions--.

Page 21, line 23, change "Fig. 2" to --Fig. 3--.

Page 22, line 3, change "Fig. 2" to --Fig. 3--.

IN THE CLAIMS:

Please amend claims 10-11 and 22-24 as follows:

Claim 10, line 2, change "any of claims 1 to 9" to --claim 1--.

Claim 11, line 2, change "any of claims 1 to 9" to --claim 1--.

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Claim 22, line 2, change "any of claims 12 to 21" to --claim 12--.

Claim 23, line 2, change "any of claims 12 to 21" to --claim 12--.

Claim 24, line 2, change "any of claims 12 to 21" to --claim 12--.

REMARKS

Claims 1-26 are pending. The specification is amended; and claims 10-11 and 22-24 are amended to eliminate multiple dependencies. Prompt and favorable consideration on the merits is respectfully requested.

Respectfully submitted,



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